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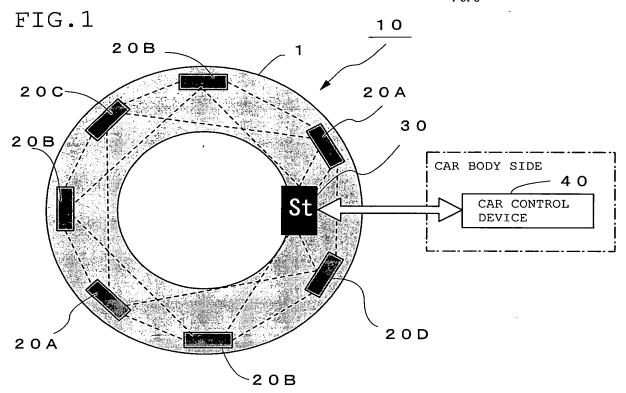
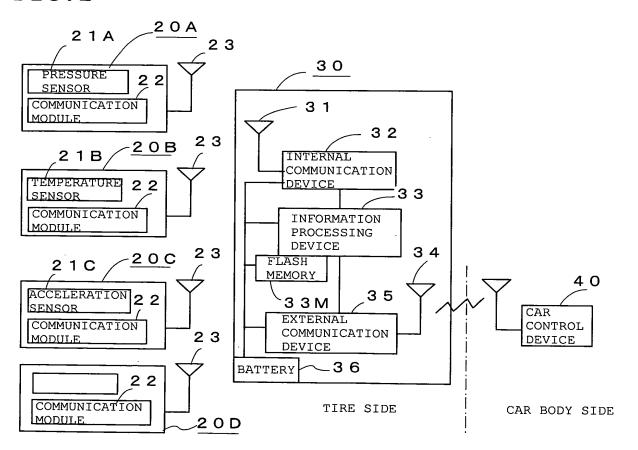
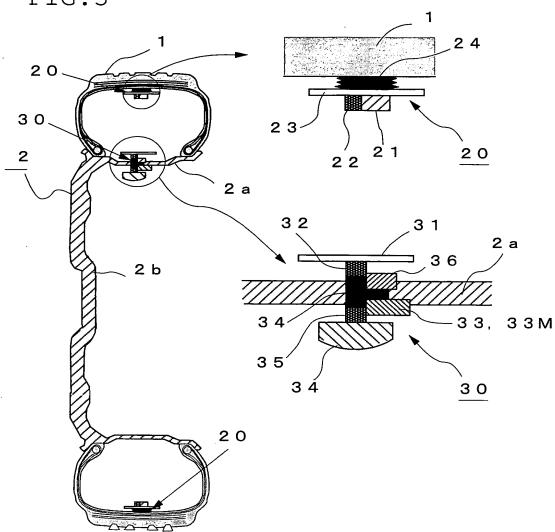


FIG.2



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FIG.3



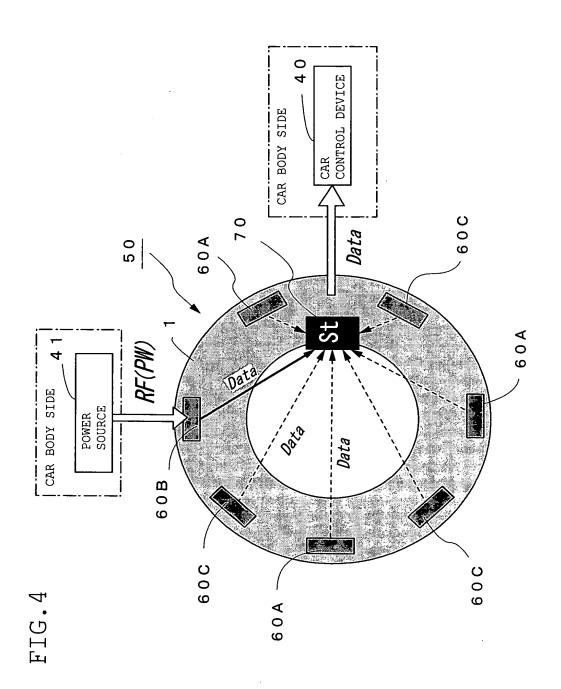
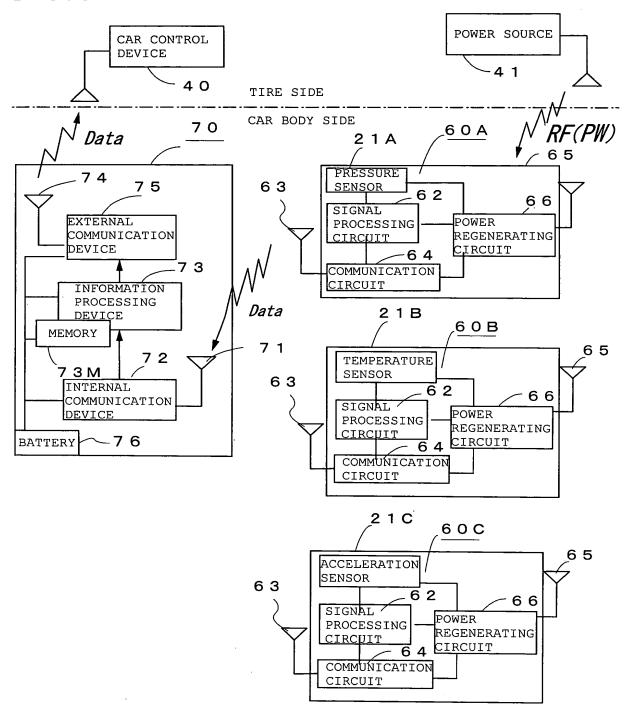
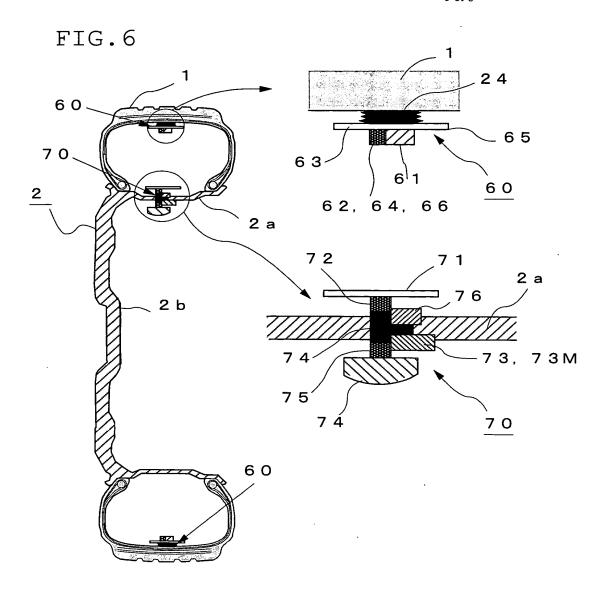


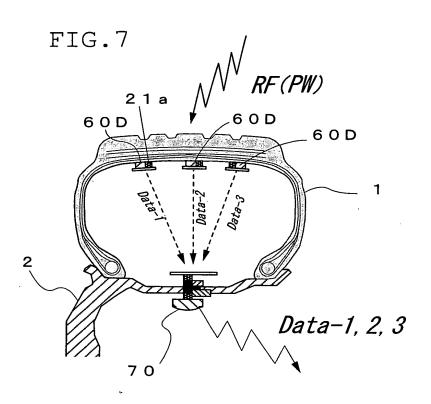
FIG.5



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FIG.8(a) PRIOR ART FIG.8(b) PRIOR ART

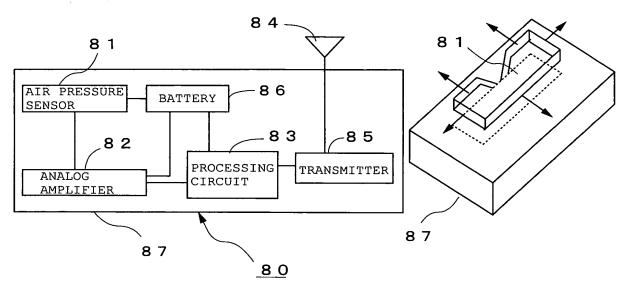


FIG.9 PRIOR ART 0000 90 90 9 5 93 9 0 P INTERFACE CIRCUIT SEQUENCER CLOCK TRANSMITTER CIRCUIT CIRCUIT CIRCUIT 9 1 9 2 9 4

## FIG.10(a) PRIOR ART

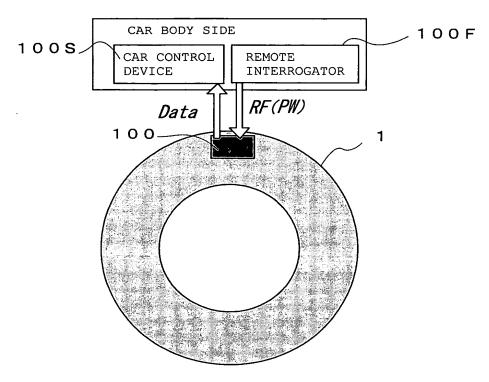


FIG.10(b) PRIOR ART

